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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/418,035 10/14/1999 PAT 6,653,695

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 10-293901 10/15/1998

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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## TITLE

Semiconductor device with an improved gate electrode pattern and a method of manufacturing the same

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